

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Vinodh R. Cuppa, et al.

Examiner:

Unassigned

Serial No:

09/855,241

Art Unit:

2184

Filed:

May 15, 2001

Docket:

YOR920000809US1 (14138)

For: A METHOD AND APPARATUS FOR

REDUCING LOGIC ACTIVITY IN A

MOCROPROCESSOR

Dated:

December 6, 2001

RECEIVED

DEC 2 7 2001

Technology Center 2100

Assistant Commissioner for Patents United States Patent and Trademark Office Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

- David Brooks, et al.(1999)"Dynamically Exploiting Narrw Width
 Operands to Improve Processor Power and Performance", <u>IEEE</u> pgs. 13-22; and
- 2. Ramon Canal, et al. (2000) "Very Low Power Pipelines Using Significance Compression", <u>IEEE</u> pgs. 181-190.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner of Patents and Trademarks, Washington, D.C. 20231 on December 6, 2001.

Dated: December 6, 2001

Michelle Mustafa

G:nec\IDS\regularids

Applicants are submitting copies of the above-cited references.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,

William C. Roch

Registration No. 24,972

Scully, Scott, Murphy & Presser 400 Garden City Plaza Garden City, New York 11530 (516) 742-4343

WCR/sf

Serial No. Serial No. Serial No. May 15, 2001 Title: A METHOD AND APPARATUS FOR REDUCING LOGIC ACTIVITY IN A MICROPROCESSOR RECEIVED Address to: Assistant Commissioner for Patents Washington, D.C. 20231 Technology Center 2100 37 CFR 1.97(b) The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application; within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or before the mailing date of a first Office Action on the merits, whichever event occurs last.			OF INFORMATION DISCLO (Under 37 CFR 1.97(b) or 1.97	İ	Docket No. YOR92000809US1 (14138)
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In Re Application Of: V	8 2001		
Serial No. 78.4	Filing Date May 15, 2001	Examiner Unassigned	Group Art Unit 2184
Title: A METHOD AND APPA	RATUS FOR REDUCING LO	GIC ACTIVITY IN A MICROPR	OCESSOR RECEIVED
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as described belo Charge the Credit any Charge and Certificate of The Company of the Certificate of The Cer	mmissioner is hereby authorized w. A duplicate copy of this she amount of y overpayment. In additional fee required. Transmission by Facsimile* ocument and authorization to charging facsimile transmitted to the Unite	Certificate of Mailin I certify that this document on 12/6/01 first class mail under 37 C.F.I. Assistant Commissioner for 20231 Signature of Person M. Michelle	g by First Class Mail t and fee is being deposited with the U.S. Postal Service as R. 1.8 and is addressed to the Patents Washington, D.C. failing Correspondence
	Name of Person Signing Certificate	Typed or Printed Name of Pe	erson Mailing Correspondence
deposit account.		_ Dated: December 6, 2001	
WCR/sf cc:			

P10A/REV01